

WHAT IS CLAIMED IS:

1 1. A laser driver for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, comprising:

4 an internal clock production circuit configured to produce an internal clock
5 synchronous with the record data signal; and

6 a strobe circuit configured to strobe the record data signal according to the
7 internal clock.

1 2. A laser driver according to claim 1, wherein the internal clock
2 production circuit comprises:

3 a phase comparater configured to produce a phase difference signal
4 representing a phase difference between the record data signal and the internal clock;

5 a charge pump circuit configured to smooth the phase difference signal;

6 a filter configured to limit a bandwidth of the phase difference signal; and a
7 voltage controlled oscillator configured to transmit as the internal clock a signal having a
8 frequency determined based on a center frequency designated by a laser driver controller and
9 a control voltage received from the filter.

1 3. A laser driver for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:

4 a mark/space verification circuit configured to verify according to the
5 recording clock whether the record data represented by the record data signal is a mark or
6 space; and

7 a recording clock inverter configured to invert a phase of a received recording
8 clock;

9 wherein when said mark/space verification circuit detects a mark or space
10 whose length is equal to or smaller than a predetermined length, said recording clock inverter
11 inverts the phase of the recording clock.

12 4. A laser driver according to claim 3, wherein when the phase of the
13 recording clock is inverted, a shift value representing the magnitude of a shift by which the
14 timing of an edge of a record pulse is shifted equal to about a half cycle of the recording

15 clock is added to or subtracted from a control value based on which the timing of the edge of
16 the record pulse is controlled.

1 5. An optical disk system including the laser driver according to claim 3,
2 further comprising a sample-and-hold circuit configured to sample and hold a waveform of a
3 signal to be recorded on a recording medium or a waveform of a signal to be regenerated
4 from a recording medium; and wherein a control signal, based on which a phase of a
5 recording clock or a strobing clock is changed by substantially 180°, is used to substantially
6 cause a 180° change to the sampling timing at which said sample-and-hold circuit samples a
7 waveform.

1 6. An optical disk system including the laser driver according to claim 3,
2 further comprising:
3 a signal processor configured to transmit the record data signal to the laser
4 driver;
5 a laser diode configured to be driven by the laser driver; and
6 a driver controller configured to control the laser driver and the signal
7 processor.

1 7. A laser driver for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:
4 a recording clock inverter configured to invert a phase of a received recording
5 clock; and
6 a phase error detector configured to detect a phase error between a strobing
7 clock used to strobe the record data signal and the record data signal;
8 wherein when a phase difference between an edge of the record data signal
9 and a strobing edge of the strobing clock becomes equal to or smaller than a predetermined
10 value, said recording clock inverter inverts the recording clock.

1 8. A laser driver according to claim 7, wherein when the phase of the
2 recording clock is inverted, a shift value representing the magnitude of a shift by which the
3 timing of the edge of a record pulse is shifted equal to about a half of the cycle of the
4 recording clock is added to or subtracted from a control value based on which the timing of
5 the edge of the record pulse is controlled.

1 9. An optical disk system including the laser driver according to claim 7,
2 further comprising a sample-and-hold circuit configured to sample and hold a waveform of a
3 signal to be recorded on a recording medium or a waveform of a signal to be regenerated
4 from a recording medium; and wherein a control signal, based on which a phase of a
5 recording clock or a strobing clock is changed by substantially 180°, is used to substantially
6 cause a 180° change to the sampling timing at which said sample-and-hold circuit samples a
7 waveform.

1 10. A laser driver for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:

4 a phase-locked loop configured to produce an internal clock synchronous with
5 the recording clock and a group of clocks including a plurality of clocks that are out of phase
6 with the internal clock by predetermined magnitudes;

7 a write strategy controller configured to control a timing of an edge of a write
8 strategy by utilizing timings of edges of the group of clocks;

9 a selector configured to select a strobing clock, which is used to strobe a
10 received record data signal, from the group of clocks; and

11 a mark/space verification circuit configured to verify according to the internal
12 clock whether the record data represented by the record data signal is a mark or space;

13 wherein when said mark/space verification circuit detects a mark or space
14 whose duration is equal to or smaller than a predetermined duration, a clock substantially
15 180° out of phase with the clock currently selected as a current strobing clock is adopted as a
16 new strobing clock to replace the current strobing clock.

1 11. A laser driver according to claim 10, wherein when a clock
2 substantially 180° out of phase with the clock currently selected as a current strobing clock is
3 adopted as a new strobing clock, a shift value representing the magnitude of a shift by which
4 the timing of the edge of a record pulse is shifted equal to about a half of the cycle of the
5 recording clock is added to or subtracted from a control value based on which the timing of
6 the edge of a record pulse is controlled.

1 12. A laser driver for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:

4 a variable delay circuit configured to vary a phase of the record data signal or
5 the recording clock; and

6 a mark/space verification circuit configured to verify, according to the
7 recording clock, whether the record data represented by the record data signal is a mark or
8 space;

9 wherein when said mark/space verification circuit detects a mark or space
10 whose length is equal to or smaller than a predetermined length, a delay to the phase of the
11 record data signal or the recording clock to be produced by said variable delay circuit is
12 varied.

1 13. A laser driver according to claim 12, further comprising a delay
2 controller configured to use a group of clocks to strobe the record data signal; wherein said
3 mark/space verification circuit is configured to use an internal clock synchronous with the
4 recording clock to strobe the record data signal; and wherein said delay controller is
5 configured to vary the magnitude of the delay to be produced by the variable delay circuit
6 based on results of strobing the group of clocks, the group of clocks including a plurality of
7 clocks that are out of phase with the internal clock by predetermined magnitudes.

1 14. A laser driver for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:

4 a phase-locked loop configured to produce an internal clock synchronous with
5 the recording clock and a group of clocks including a plurality of clocks that are out of phase
6 with the internal clock by predetermined magnitudes;

7 a write strategy controller configured to control a timing of an edge of a write
8 strategy by utilizing the timings of the edges of the group of clocks; and

9 a selector configured to select a strobing clock, which is used to strobe a
10 received record data signal, from the group of clocks;

11 wherein said selector is configured to select a strobing clock, which is used to
12 produce a write strategy comprising a recording waveform from clocks, which are used for
13 strobing, according to results of strobing the record data signal using the clocks included in

14 the group of clocks so as to maximize a phase difference between the edge of the record data
15 signal and the strobing edge of the strobing clock.

1 15. A laser driver according to claim 14, wherein when a strobing clock is
2 selected from the group of clocks, the phase of a reference clock serving as a reference of a
3 control value based on which the timing of the edge of a record pulse is controlled is agreed
4 with the phase of the strobing clock.

1 16. An optical disk system including the laser driver according to claim 14,
2 further comprising a sample-and-hold circuit configured to sample and hold a waveform of a
3 signal to be recorded on a recording medium or a waveform of a signal to be regenerated
4 from a recording medium; and wherein when clocks from which a strobing clock is to be
5 selected are changed, the sample-and-hold timing is changed so that the timing of sampling
6 the waveform of a signal to be recorded on a recording medium included in said optical disk
7 system or the waveform of a signal to be regenerated from the recording medium during
8 sample-and-hold will agree with the timing of the edge of the strobing clock.

1 17. A laser driver for producing the waveform a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:

4 a phase-locked loop configured to produce an internal clock synchronous with
5 the recording clock and a group of clocks including a plurality of clocks that are out of phase
6 with the internal clock by predetermined magnitudes;

7 a write strategy controller configured to control a timing of an edge of a write
8 strategy by utilizing the timings of the edges of the group of clocks; and

9 a phase error detector configured to detect a phase error between a strobing
10 clock used to strobe a received record data signal and the record data signal;

11 wherein based on result of detection performed by said phase error detector, a
12 strobing clock is selected from the group of clocks so as to maximize a phase difference
13 between the edge of the record data signal and the strobing edge of the strobing clock.

1 18. A laser driver according to claim 17, wherein when a strobing clock is
2 selected from the group of clocks, the phase of a reference clock serving as a reference of a
3 control value based on which the timing of the edge of a record pulse is controlled is agreed
4 with the phase of the strobing clock.

1 19. An optical disk system including the laser driver according to claim 17,
2 further comprising a sample-and-hold circuit configured to sample and hold a waveform of a
3 signal to be recorded on a recording medium or a waveform of a signal to be regenerated
4 from a recording medium; and wherein when clocks from which a strobing clock is to be
5 selected are changed, the sample-and-hold timing is changed so that the timing of sampling
6 the waveform of a signal to be recorded on a recording medium included in said optical disk
7 system or the waveform of a signal to be regenerated from the recording medium during
8 sample-and-hold will agree with the timing of the edge of the strobing clock.

1 20. A laser driver for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:
4 a phase-locked loop configured to produce an internal clock synchronous with
5 the recording clock and a group of clocks including a plurality of clocks that are out of phase
6 with the internal clock by predetermined magnitudes;
7 a write strategy controller configured to control a timing of an edge of a write
8 strategy by utilizing timings of edges of the group of clocks; and
9 a variable delay circuit configured to vary a phase of the record data signal or
10 the recording clock;
11 wherein based on results of strobing the record data signal according to the
12 plurality of clocks included in the group of clocks, a magnitude of a delay to the phase of the
13 record data signal or the recording clock to be produced by said variable delay circuit is
14 controlled so as to maximize a phase difference between an edge of the record data signal and
15 a strobing edge of the strobing clock.

1 21. A laser driver for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:
4 a phase-locked loop configured to produce an internal clock synchronous with
5 the recording clock and a group of clocks including a plurality of clocks that are out of phase
6 with the internal clock by predetermined magnitudes;
7 a write strategy controller configured to control a timing of an edge of a write
8 strategy by utilizing timings of edges of the group of clocks;

9 a phase error detector configured to detect a phase error between a strobing
10 clock used to strobe the record data signal and the record data signal; and
11 a variable delay circuit configured to varying a phase of the record data signal
12 or the recording clock;
13 wherein based on the results of said phase error detector, the magnitude of a
14 delay to be produced by said variable delay circuit is controlled so as to maximize a phase
15 difference between the edge of the record data signal and the phase of the strobing edge of
16 the strobing signal.

1 22. A method for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, the method comprising:

4 producing an internal clock synchronous with the record data signal;
5 strobing the record data signal according to the internal clock to obtain a
6 driving signal; and
7 driving a laser diode with the driving signal.

1 23. A method for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, the method comprising:

4 verifying according to the recording clock whether the record data represented
5 by the record data signal is a mark or space;
6 inverting a phase of a recording clock, when a mark or space whose length is
7 equal to or smaller than a predetermined length is detected during the verifying; and
8 producing, utilizing the inverted phase of the recording clock, a driving signal
9 to be used to drive a laser diode.

1 24. A method for producing the waveform of a driving signal, which is
2 used to drive a laser diode, according to a record data signal that represents record data to be
3 recorded on a recording medium, and a recording clock, comprising:

4 producing an internal clock synchronous with the recording clock and a group
5 of clocks including a plurality of clocks that are out of phase with the internal clock by
6 predetermined magnitudes;
7 controlling a timing of an edge of a write strategy by utilizing timings of edges
8 of the group of clocks;

9 verifying according to the internal clock whether the record data represented
10 by the record data signal is a mark or space;
11 selecting a strobing clock, which is used to strobe a received record data
12 signal, from the group of clocks, wherein when a mark or space whose duration is equal to or
13 smaller than a predetermined duration is detected during the verifying, a clock substantially
14 180° out of phase with the clock currently selected as a current strobing clock is adopted as a
15 new strobing clock to replace the current strobing clock; and
16 producing, using the new strobing clock, a driving signal to be used to drive a
17 laser diode.